

FIG. 10

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graph TD; 1102[Receive Data via a Data Receiving Device] --> 1104[Decimate Sampling Rate of Received Data by a Factor D]; 1104 --> 1106[Store the Data at the Decimated Rate into a Memory]; 1106 --> 1108[Read Stored Data]; 1108 --> 1110[Interpolate Decimated Rate of the Stored Data by a Factor I]; 1110 --> 1112[Output Data to a Processing Module];
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1102 Receive Data via a Data Receiving Device

1104 Decimate Sampling Rate of Received Data by a Factor D

1106 Store the Data at the Decimated Rate into a Memory

1108 Read Stored Data

1110 Interpolate Decimated Rate of the Stored Data by a Factor I

1112 Output Data to a Processing Module

FIG. 11

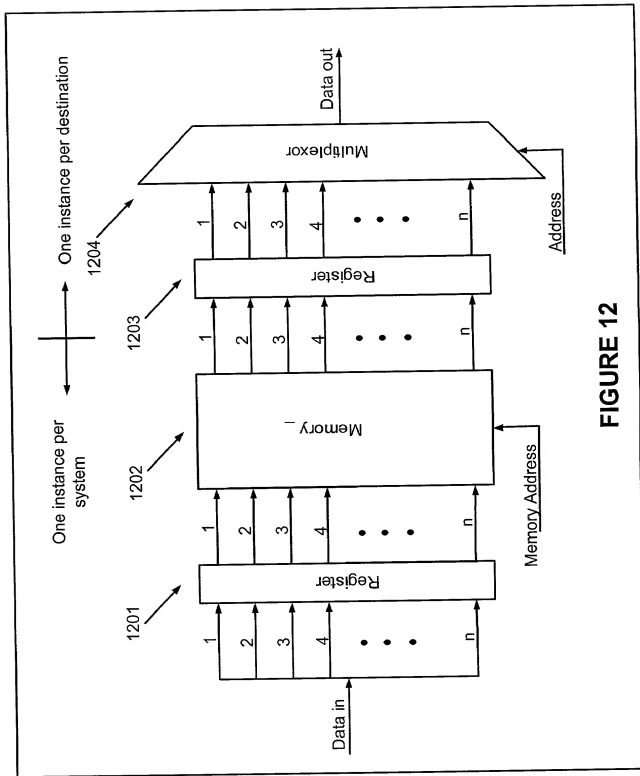


FIGURE 12